Title: **PLC Basics** Test: 1

Course: Introduction to Automation Unit: Introduction to PLC CLO: 1

Name \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ Date \_\_\_\_\_\_\_\_\_\_\_\_\_\_

**Objectives**

1. Student shall demonstrate comprehension basic functions of a PLC.
2. Student shall analyze a PLC program to determine the program’s functionality.

**Assessment**

Students shall demonstrate a comprehension of the objectives listed above by scoring a minimum of 75% on this Test. Grading shall be based on the instructor answer key.

**Instructions**

Select the best answer for each multiple-choice question below.

1. How many instructions can the MicroLogix 1100 PLC perform at any one time?
   1. 4
   2. 3
   3. 2
   4. 1
2. What does the three-letter acronym, PLC mean?
   1. Programmable Ladder Computer
   2. Proportional Logic Controller
   3. Programmable Logic Controller
   4. Programmable Ladder Controller
3. Between inputs and outputs, which actually control elements in the field?
   1. Inputs
   2. Outputs
   3. Both
   4. Neither
4. How are the inputs to a PLC protected?
   1. Through a fuse
   2. Through a resettable circuit breaker
   3. Through an LED and photo transistor
   4. Through a current limiting zener
5. What component is used to relay an output signal?
   1. A coil
   2. A dry contact
   3. An LED and photo transistor
   4. A current limiting Zener
6. A collection of \_\_\_\_\_ makes up a word.
   1. Places
   2. Binaries
   3. Bits
   4. Variables
7. A word has \_\_\_\_ places.
   1. 1
   2. 4
   3. 8
   4. 16
8. The binary number system has how many digits?
   1. 1
   2. 2
   3. 8
   4. 10
9. Why is the binary number system used for motor control/ladder logic?
   1. It’s simple
   2. It has only two states
   3. It’s not used for motor control logic
   4. None of the above
10. List the order of operation of a PLC.
    1. Scan inputs, write outputs, evaluate ladder logic
    2. Evaluate each rung reading inputs and driving outputs that are in the rung.
    3. Scan input, evaluate ladders logic, write outputs
    4. Inputs, logic and outputs are all read/evaluated/written in parallel at the same time.
11. Input addresses start with the letter \_\_\_\_
12. Output addresses start with the letter \_\_\_\_
13. Internal memory bits start with the letter \_\_\_\_
14. Review the answers for 11, 12 and 13. Describe the difference between each address type.
15. Describe the problem with the ladder logic below.

